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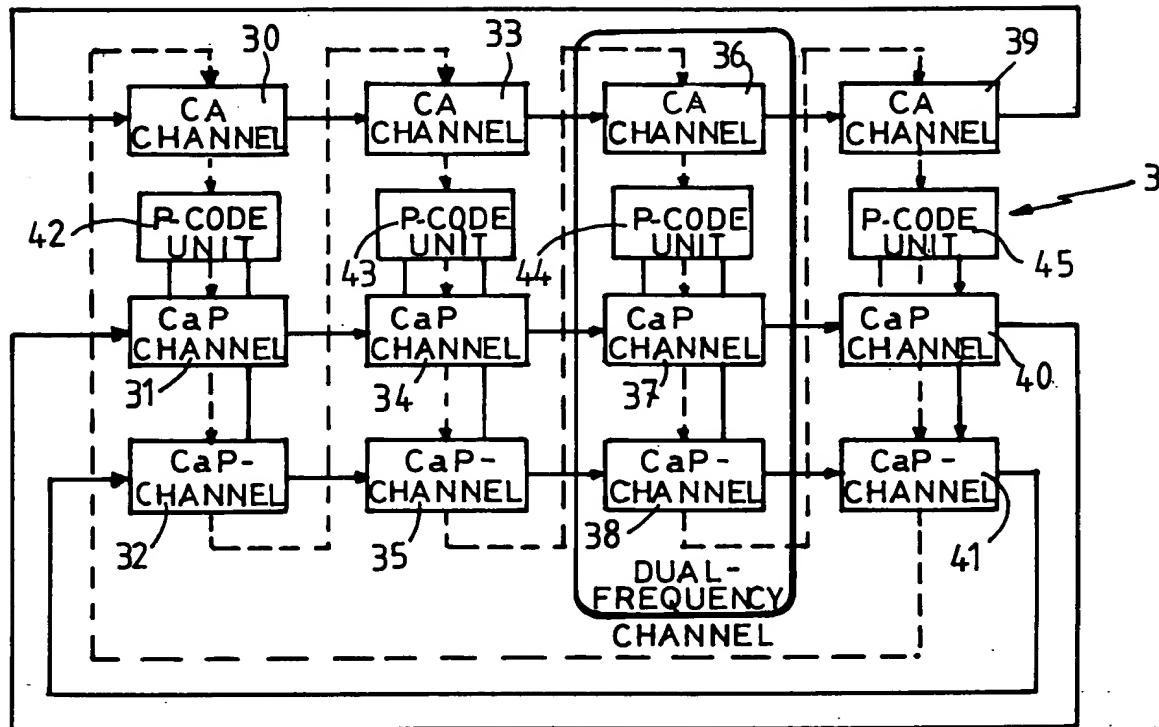


FIG. 3

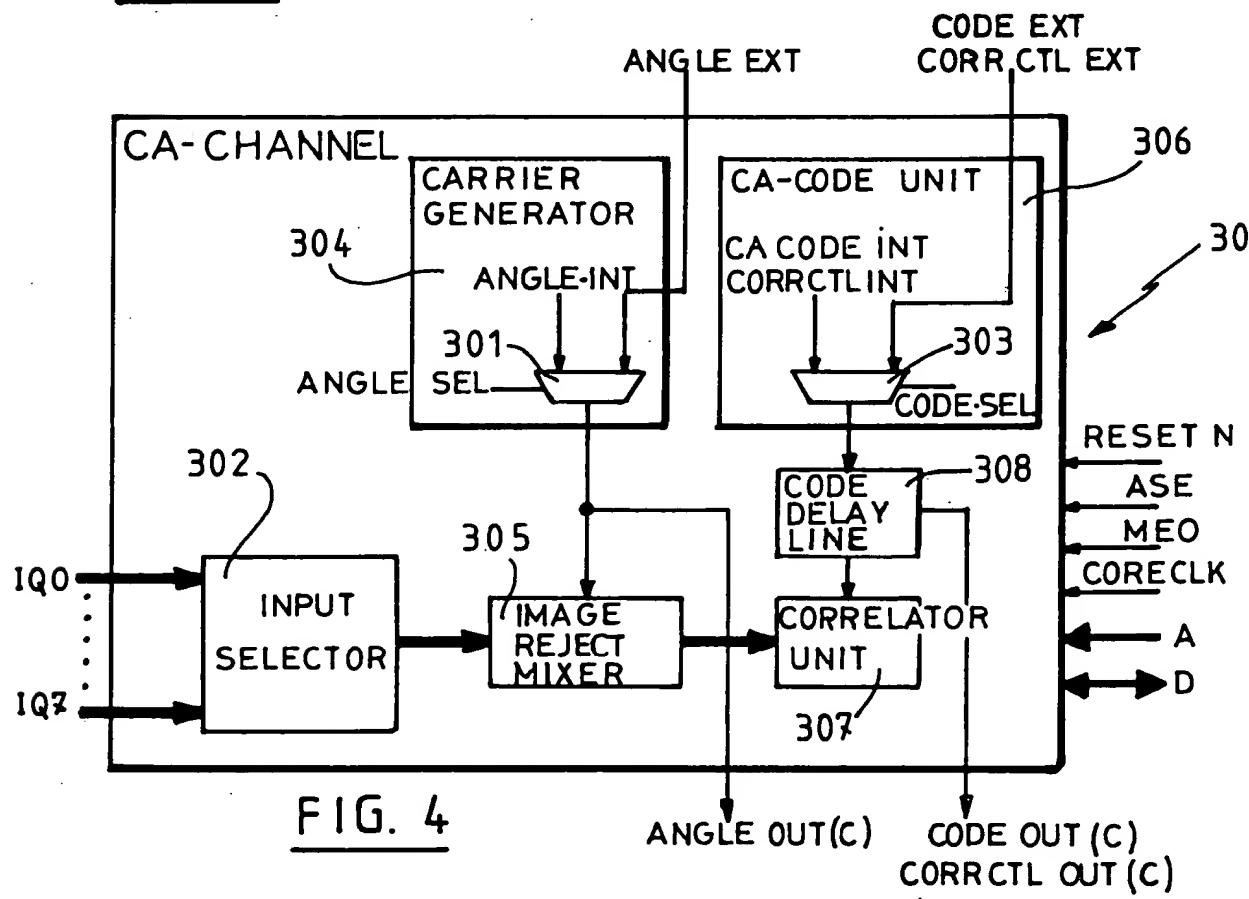


FIG. 4

FIG. 5

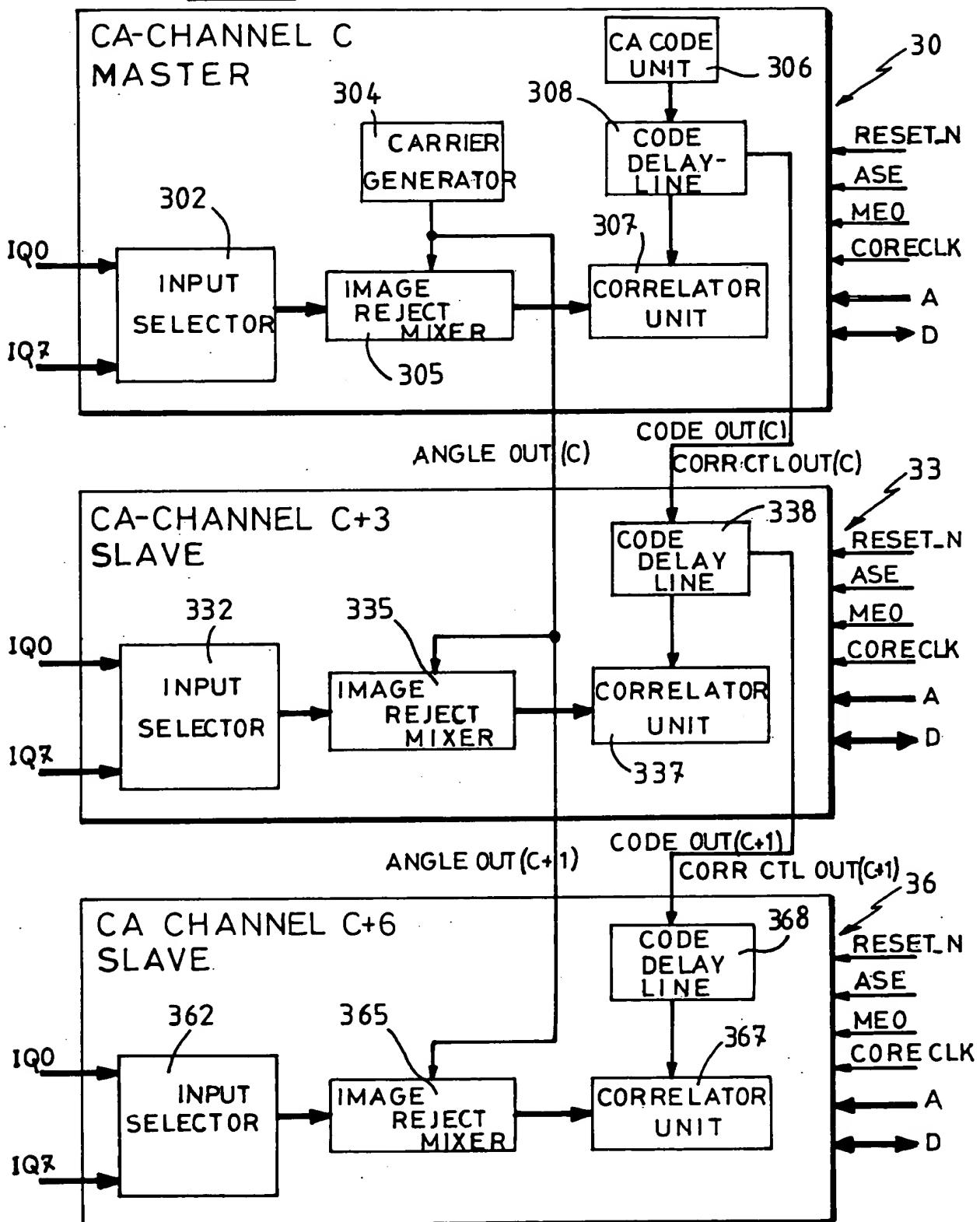
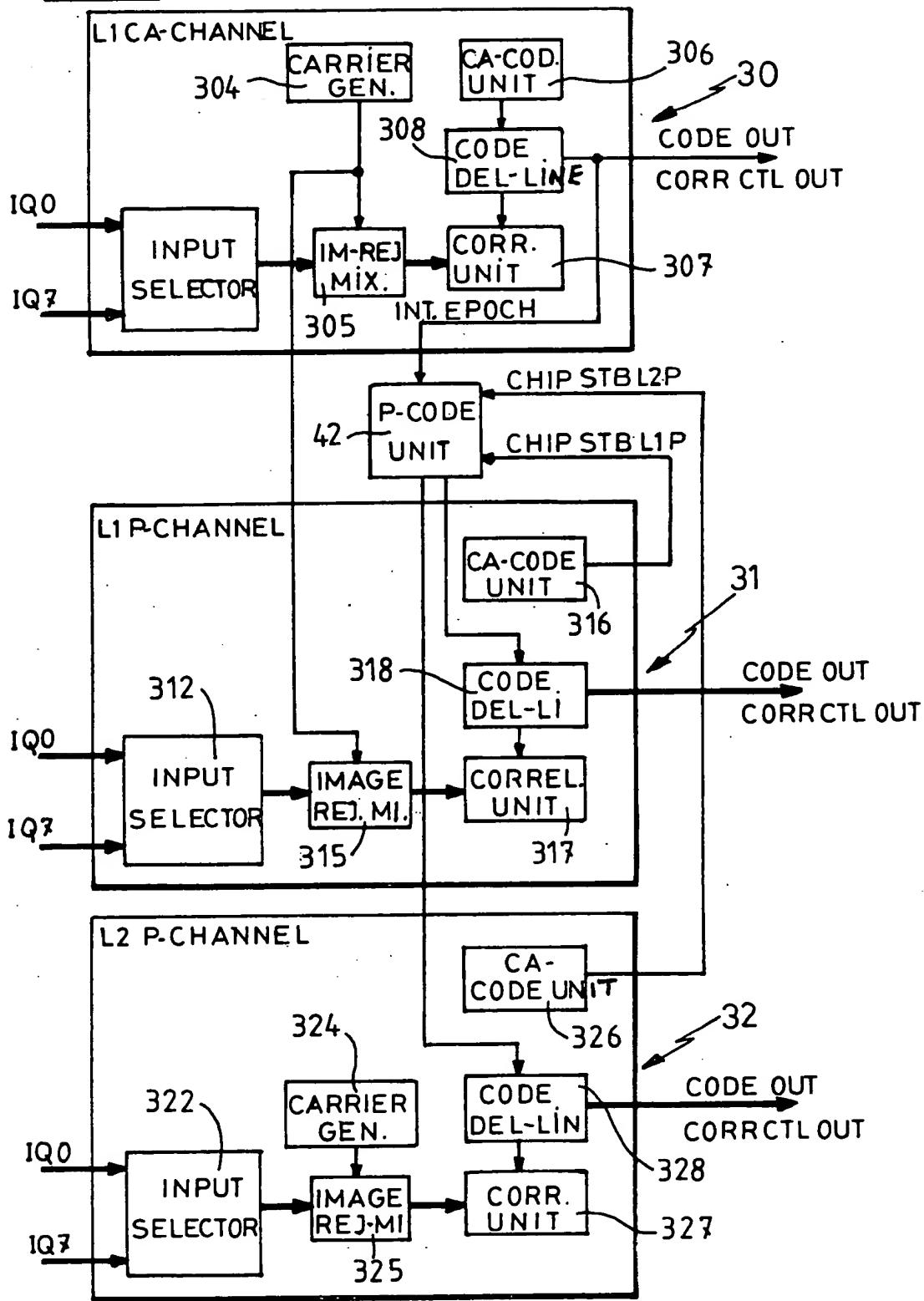


FIG. 6



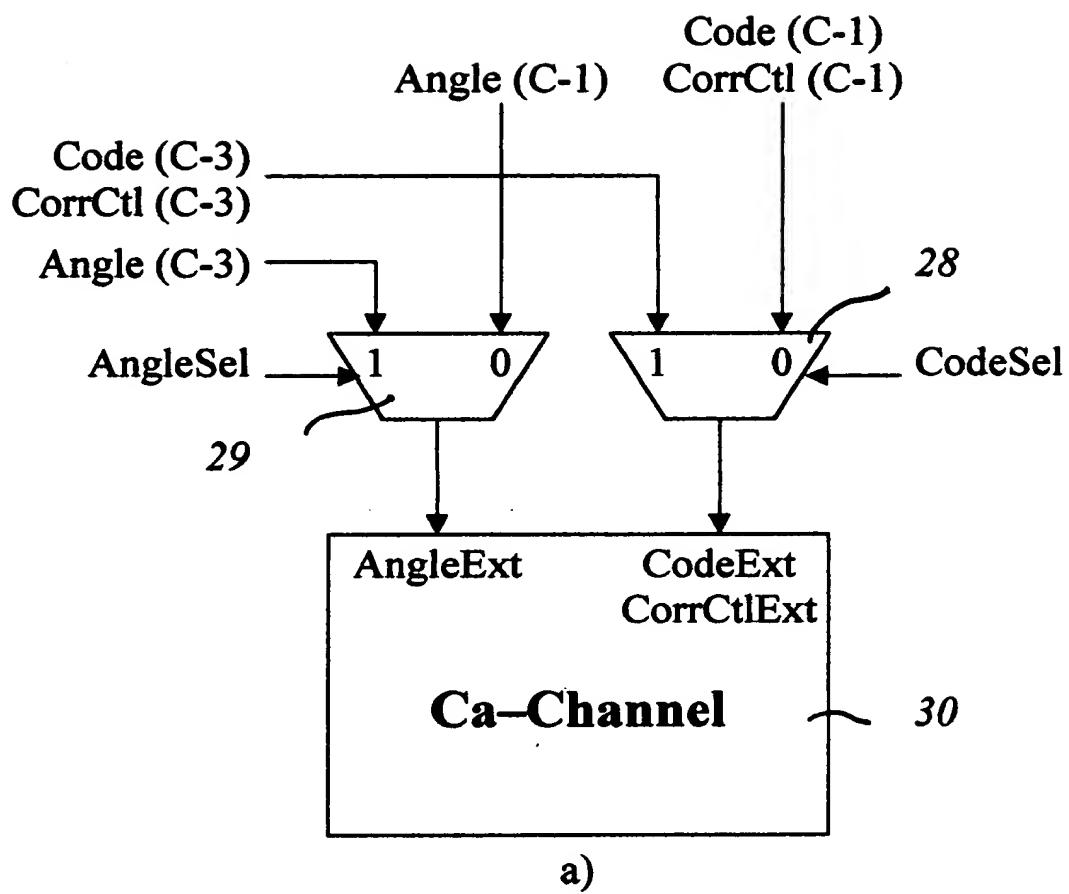


Fig. 7a

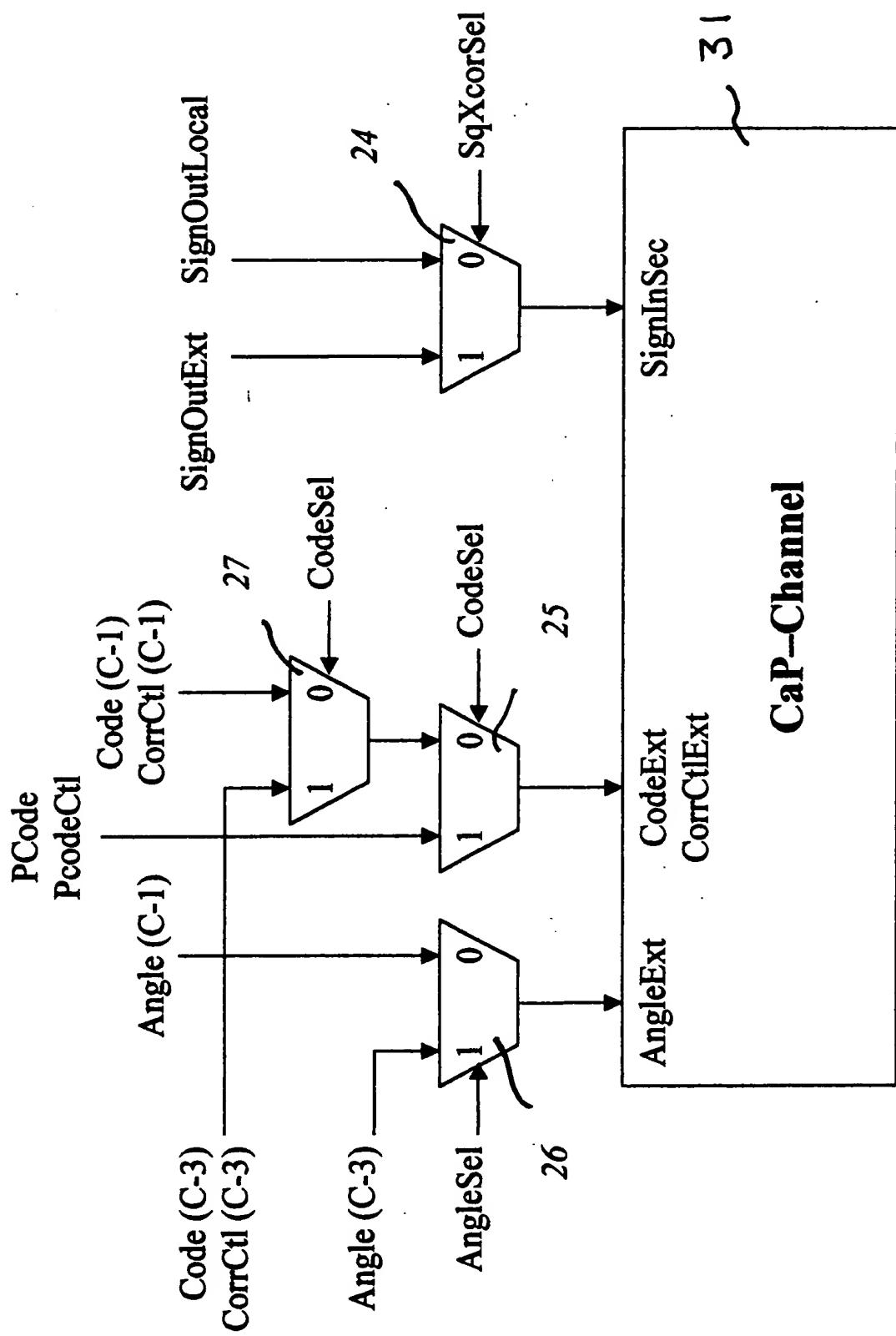
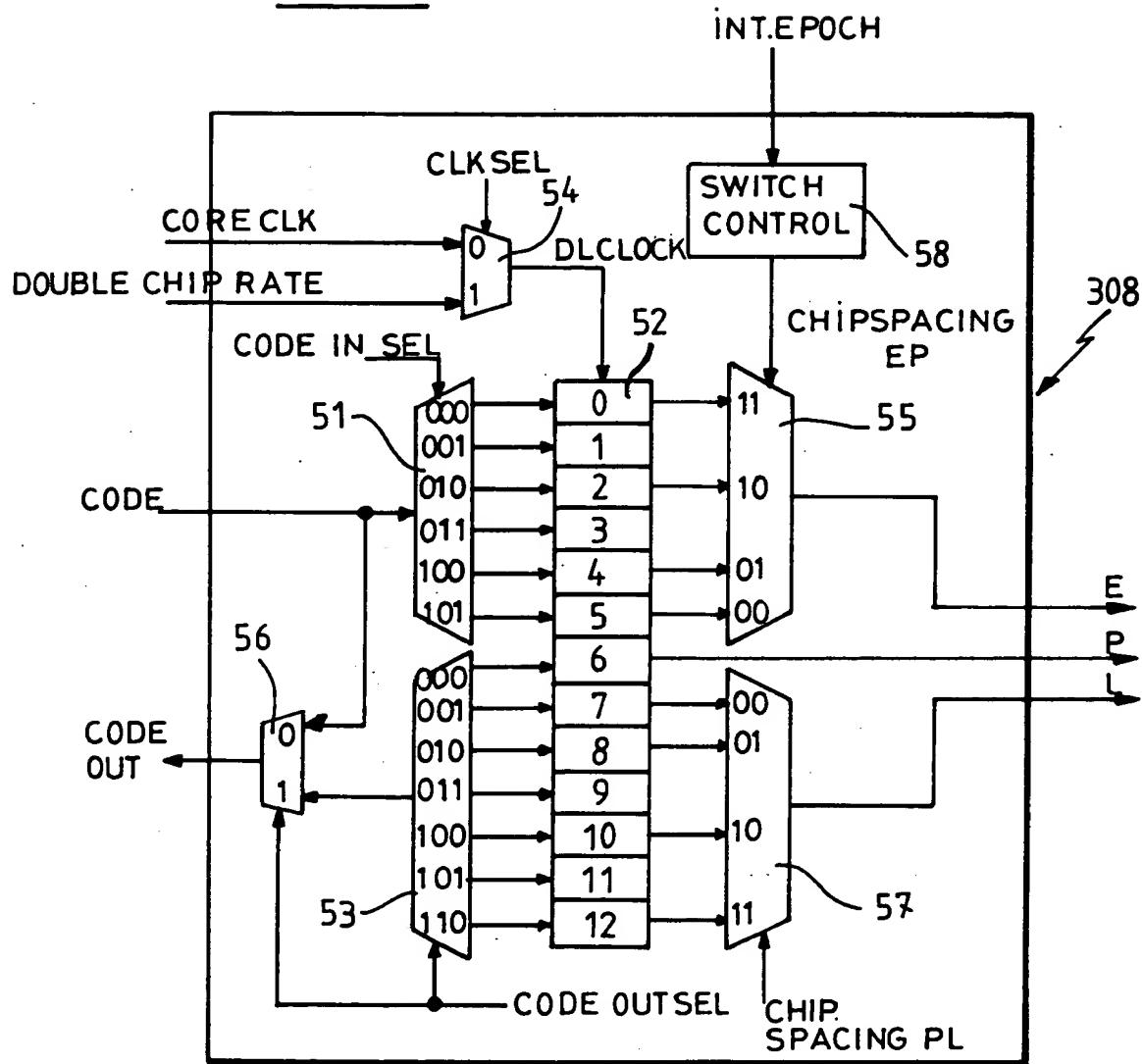
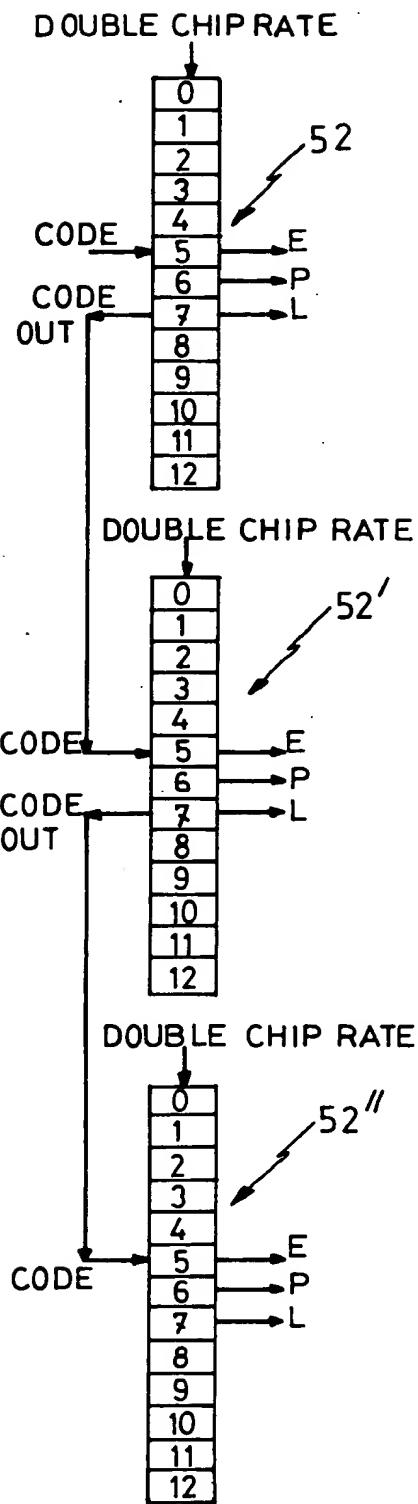


Fig. 7b
b)

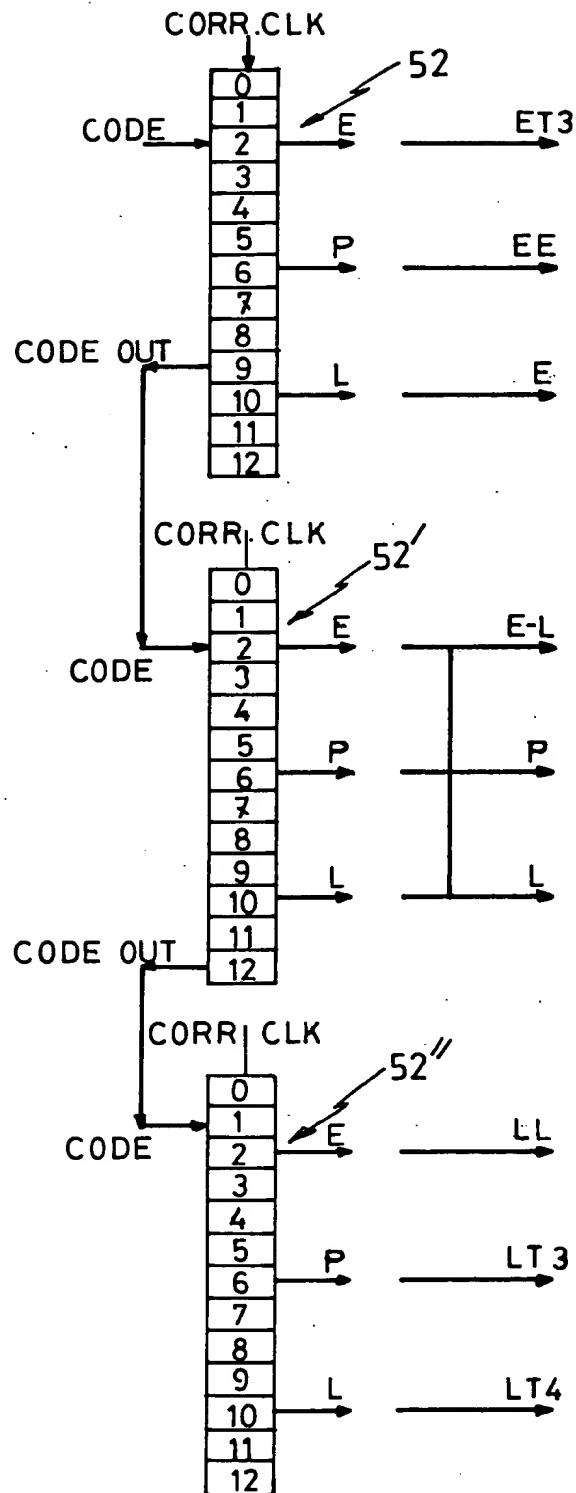
FIG. 8





(a)FAST ACQUISITION MODE

FIG. 9a



(b) MULTIPATH MITIGATION WITH
8T SPACING BETWEEN E AND L

FIG. 9b

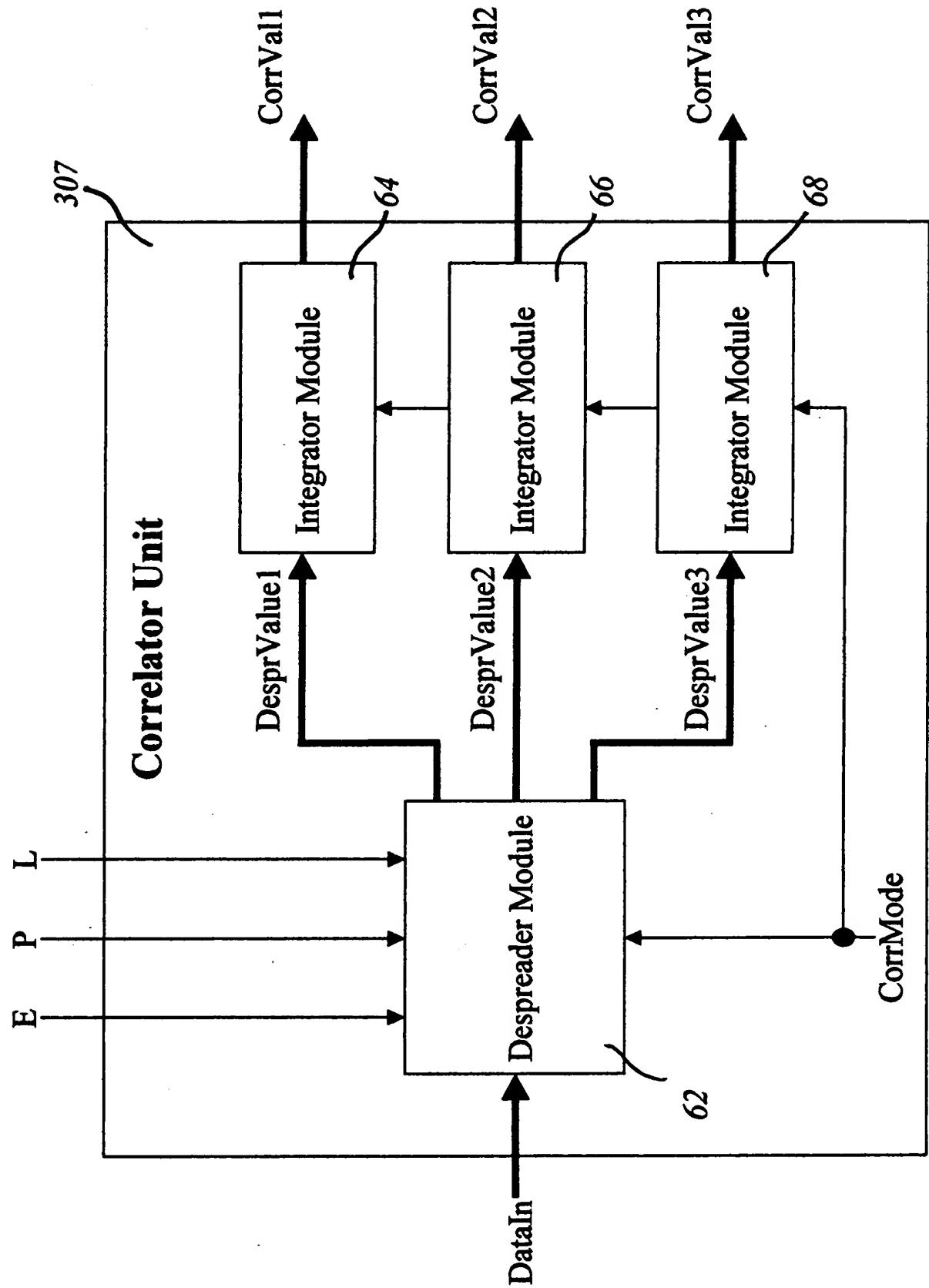


Fig. 10

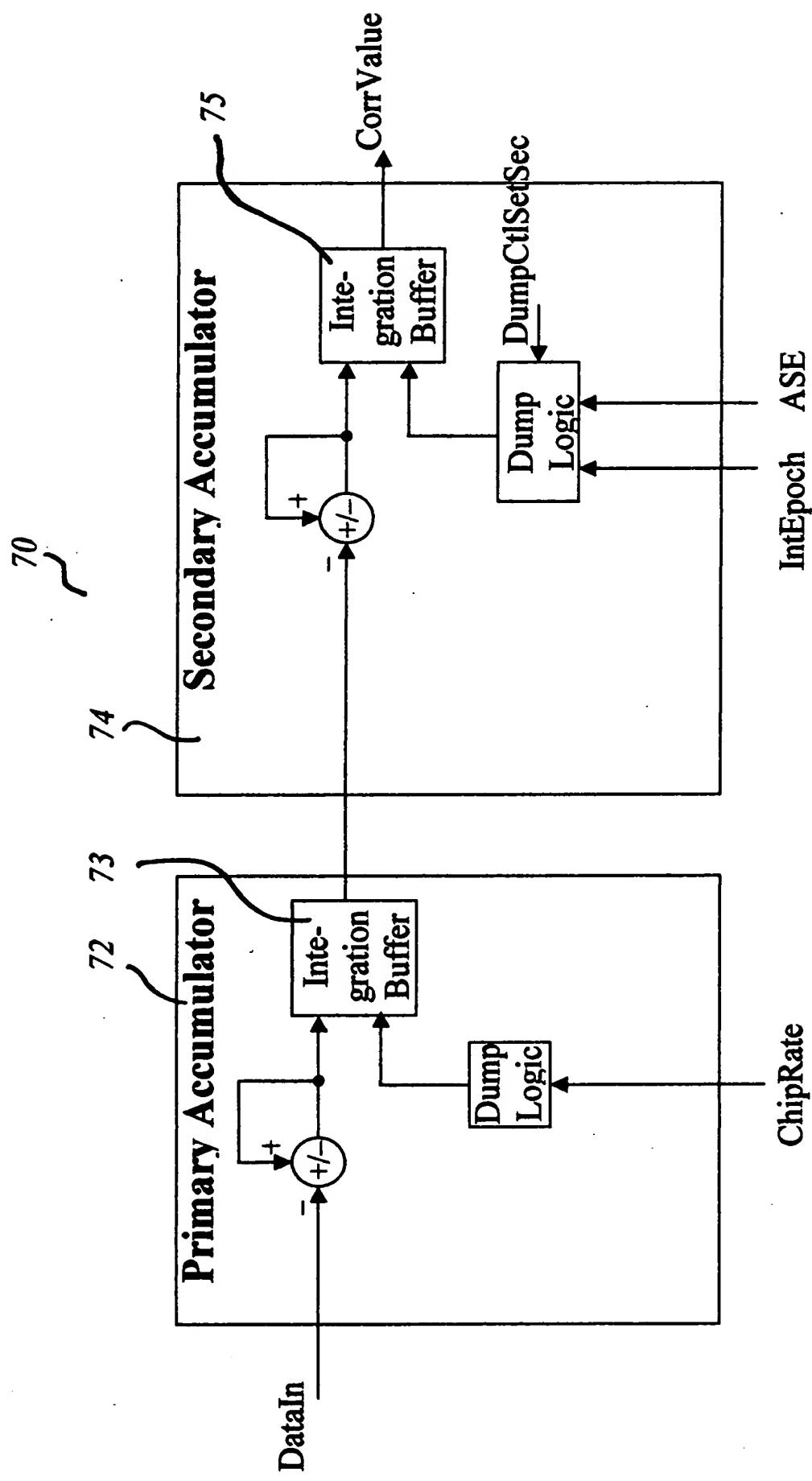


Fig. 11

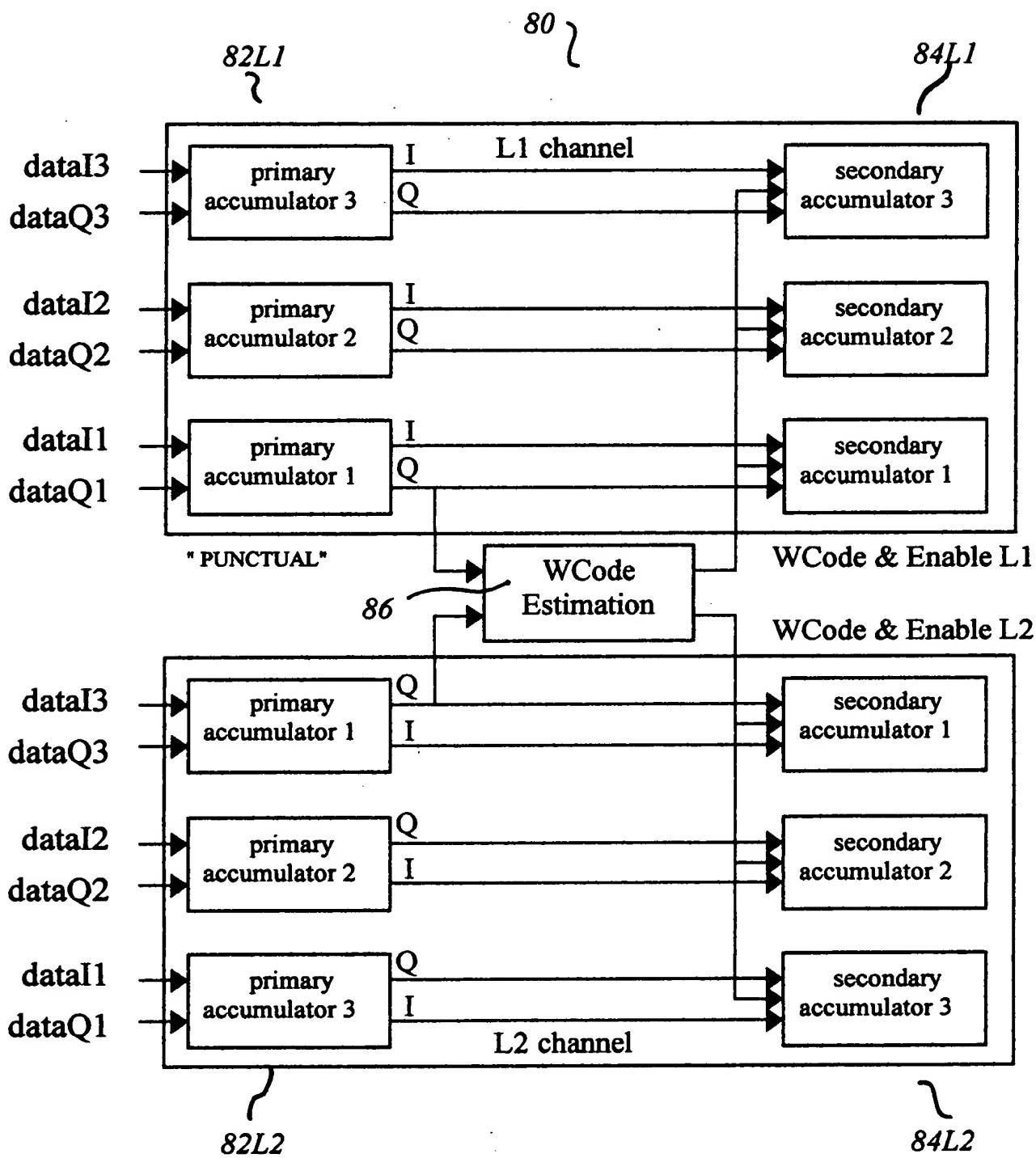
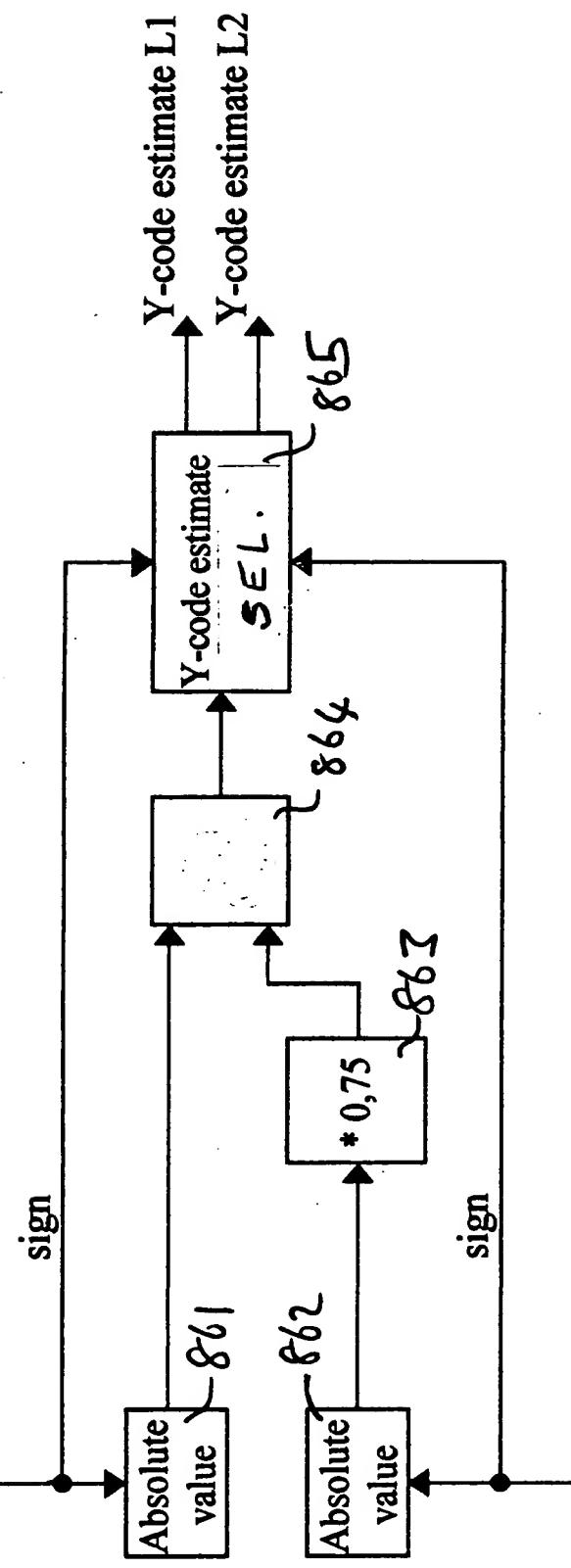


Fig. 13

Primary sum of Q0-L1 channel



Primary sum of Q0-L2 channel

Fig. 14

